

- ☐ Drafts
- ☐ Pending
- ☒ Active
  - ☒ L1: (0) (column near3 output near3 delay\$3)
  - ☒ L2: (39) (column near3 output near3 delay\$3)
  - ☒ L3: (39) (column near3 output near3 delay\$3)
  - ☒ L4: (465) (column near3 delay\$3) and seco:
  - ☒ L5: (12) (column near3 delay\$3) same seco

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO

☒ Bursals

Default operator: OR

☒ Highlight all hit terms initially

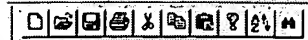
BRS form ISAR form Image Text HTML

|   | Type | L # | Hits | Search Text   | DBs                       |
|---|------|-----|------|---|---------------------------|
| 1 | BRS  | L1  | 0    | (column near3 output near3 delay\$3) same second same capacitance | US-PGPUB; USPAT; EPO; JPO |
| 2 | BRS  | L2  | 39   | (column near3 output near3 delay\$3) and second and capacitance   | US-PGPUB; USPAT; EPO; JPO |
| 3 | BRS  | L3  | 39   | (column near3 output near3 delay\$3) and second and capacitance   | US-PGPUB; USPAT; EPO; JPO |
| 4 | BRS  | L4  | 465  | (column near3 delay\$3) and second and capacitance                | US-PGPUB; USPAT; EPO; JPO |
| 5 | BRS  | L5  | 12   | (column near3 delay\$3) same second same capacitance              | US-PGPUB; USPAT; EPO; JPO |

Hits Details HTML

Ready

NUM



☐ Drafts  
☐ Pending  
☒ Active

- ☒ L1: (0) (second adj delay) same (column ac
- ☒ L2: (3) (second adj delay) same column sar
- ☒ L3: (4) (second adj delay) same column sar
- ☒ L4: (319) (second adj delay) and column ar
- ☒ L5: (17) (second adj delay) same column a

☒ Failed

Search List Browse Queue Clear

DBs US-PGPUB; USPAT; EPO; JPO

☒ Plurals

Default operator: OR

☒ Highlight all hit terms initially

BRS form IS&amp;R form Image Text HTML

|   | Type | L # | Hits | Search Text  | DBs                       |
|---|------|-----|------|--|---------------------------|
| 1 | BRS  | L1  | 0    | (second adj delay) same (column adj enable) same output same delay\$3 and period and capacitance | US-PGPUB; USPAT; EPO; JPO |
| 2 | BRS  | L2  | 3    | (second adj delay) same column same enable same output same delay\$3 and period and capacitance  | US-PGPUB; USPAT; EPO; JPO |
| 3 | BRS  | L3  | 4    | (second adj delay) same column same enable and output and delay\$3 and period and capacitance    | US-PGPUB; USPAT; EPO; JPO |
| 4 | BRS  | L4  | 319  | (second adj delay) and column and enable and output and delay\$3 and period and capacitance      | US-PGPUB; USPAT; EPO; JPO |
| 5 | BRS  | L5  | 17   | (second adj delay) same column and enable and output and delay\$3 and period and capacitance     | US-PGPUB; USPAT; EPO; JPO |

Hits Details HTML

Ready

NUM

|   | Type | L # | Hits | Search Text  | DBs                          |
|---|------|-----|------|--|------------------------------|
| 1 | BRS  | L1  | 0    | method same read\$3 same transfer\$4 same module same sens\$3 same capacitance   | US-PGPUB; USPAT;<br>EPO; JPO |
| 2 | BRS  | L2  | 119  | (method same read\$3 same transfer\$4) and module and (sens\$3 same capacitance) | US-PGPUB; USPAT;<br>EPO; JPO |
| 3 | BRS  | L3  | 43   | (method same read\$3 same transfer\$4) same (sens\$3 same capacitance)           | US-PGPUB; USPAT;<br>EPO; JPO |
| 4 | BRS  | L4  | 3    | 3 and module   | US-PGPUB; USPAT;<br>EPO; JPO |